

Photo

Ms. Jisha M V

Assistant Professor

Department of Electronics and Communication

College of Engineering Vadakara

Mobile: 9446258646

Email: jishamv@gmail.com

Qualifications:

- B.Tech in Electronics and Communication
- M.Tech in Electronics (Microwave and Radar)
- Pursuing PhD

Experience:

- Working as Assistant Professor in College of Engineering Vadakara since 06/07/2012.
- Worked as Assistant Professor in Amrita School of Engg. Kollam for 4/01/2010-05/07/2021
- Worked as Lecturer in KMCT College of Engg, Kozhikode for 15/05/2007-01/01/2010
- Worked as Lecturer in MEA Engineering College, Perinthalmanna. for 05/01/2007 - 05/05/2007
- Worked as Ad-hoc Lecturer in National Institute of Technology, Calicut for 07/06/2006 -30/11/2006
- Total service: 15years

Areas of Interest:

- Antenna Design
- Microwave and Radar
- RF Design
- Digital Signal Processing

Positions held:

- Counselling Cell Member
- Exam Cell member during 2015-16
- Academic Co-ordinator in the department during 2016-17
- Timetable Coordinator of EC department during 2012-18
- PTA Ext Member and Internal Exam Cell Member 2016-17
- ISO Co-ordinator from Department during 2014-15
- Class Advisor of 2010-2014,2014-2018 and 2018-22 batch
- Lab in charge of Electronics Circuits and Communication Engg. Lab

Subjects Taught:

- Design of Steel Structures, Design of Hydraulic Structures, Design of Concrete Structures, Geotechnical Engineering, Ground Improvement techniques, Quantity Surveying & Estimation, Irrigation & Water Resources Engineering, Sustainable Engineering, Building technology & Habitat Engineering
- Material testing lab, Transportation Engineering Lab, Environmental Engineering lab, Building Technology & Non-destructing testing lab, Drafting lab, Civil Engineering Workshop

Projects Guided:

- Smart shopping facilitator for blind 2020-21
- Chipless RF id Tag Design Using Defected Ground Structure and Spiral Resonators 2019-2020
- Microstrip patch with metamaterial and radiation horizontal 2017-2018
- Miniaturization Of Single Layer U-Slot Microstrip Patch Antenna 2016-2017

Publications:

- Jisha MV et al., "Miniaturization Of Single Layer U-Slot Microstrip Patch Antenna", International Journal of Engineering Research & Technology (IJERT) eISSN: 2319-1163 | pISSN: 2321-7308

Training Programmes Attended:

- Online Faculty Development Programme on Soft Computing Techniques organized by the Department of Mechanical Engineering, College of Engineering Perumon during February 8 th to 13th , 2021
- "IEEE R10 Panel of Conference Organizers (POCO) 2020" workshop organized by IEEE Malabar Subsection and supported by IEEE R10 during 18th & 19th December 2020
- FDP on EMBEDDED SYSTEMS organized by Dept. of Electronics and Communication Engineering during 3 - 6 March 2021
- online internship program on ADVANCED ANTENNA DESIGN, FABRICATION AND TESTING: INDUSTRIAL PRACTICES ,organized by ENTUPLE Technologies from April 08, 2021 to May20, 2021
- FDP on Pattern Analysis For Speech Signal Processing organized by Govt College of Engineering,Kozhikode15-03-2021to20-03-2021

Training Programmes Organised:

- Coordinator of Twoday National Conference on Advances in Computing Communication Signals Energy and Technology ACCSET'21, on 26,27/05/2021 at College of Engineering Vadamakara.

Professional Body Memberships:

- Member of IEEE
- IEEE Antennas and Propagation Society Member
- Life Member of The Indian Society for Technical Education (ISTE)